

# Time Table

Name of Faculty: Prof. Jagdish Kumar  
Semester: ODD (2011-2012)

Department : Electrical

	8-9 a.m	9-10 a.m	10-11 a.m	11-12 a.m	12-1 p.m	1-2 p.m	2-3 p.m	3-4 p.m	4-5 p.m
MONDAY			EN 204(L) L9	EN 307(L) Ec L11					
TUESDAY	EN 204(L) L9	EE 537(P) Microprocessor Lab				TAN104(T) AB3 T6		EE 401(P) 7E1 Control Lab	
WEDNESDAY			EN 307(L) Ec L24			TAN104(T) AB5 T1			
THURSDAY	EN 204(L) L9		EN 307(L) Ec L22	EE 537(P) Microprocessor Lab					
FRIDAY				EN 307(L) Ec L24				EN 204(L) L9	

Indicate Course No., L/T/P, Group No., Room No. in the Time Table above

Indicate Course Titles here: EN 204 Power Electronics ; EE537 (P) Power Electronics Simulation Lab

EN 307 Power Electronics ; EE 401(P) Control Lab; Tan 104 (T) Mechatronics